

Product Change Notification (PCN) & Errata Log

Historical Changes and Process Engineering Log for Cosine MPNs

1. PCN Policy

Cosine Nanoelectronics adheres to JEDEC JESD46 standards for customer notification of product and process changes. Major changes in design, wafer fab, packaging materials, or FT test methodology are announced 90 days prior to initial shipment.

2. Log Summary

PCN-2024-01	COS13487EESA: Enhanced bus ESD protection structure from ±12kV to ±15kV HBM.
PCN-2023-04	COS8552SR: Added MSOP-8 package option to catalog.
PCN-2023-01	COS4427SR: Upgraded leadframe plating to ultra-matte tin for extended solderability shelf life.

3. Form, Fit, Function Statement

All historical process enhancements maintain 100% pin-to-pin drop-in compatibility and preserve underlying electrical specifications.